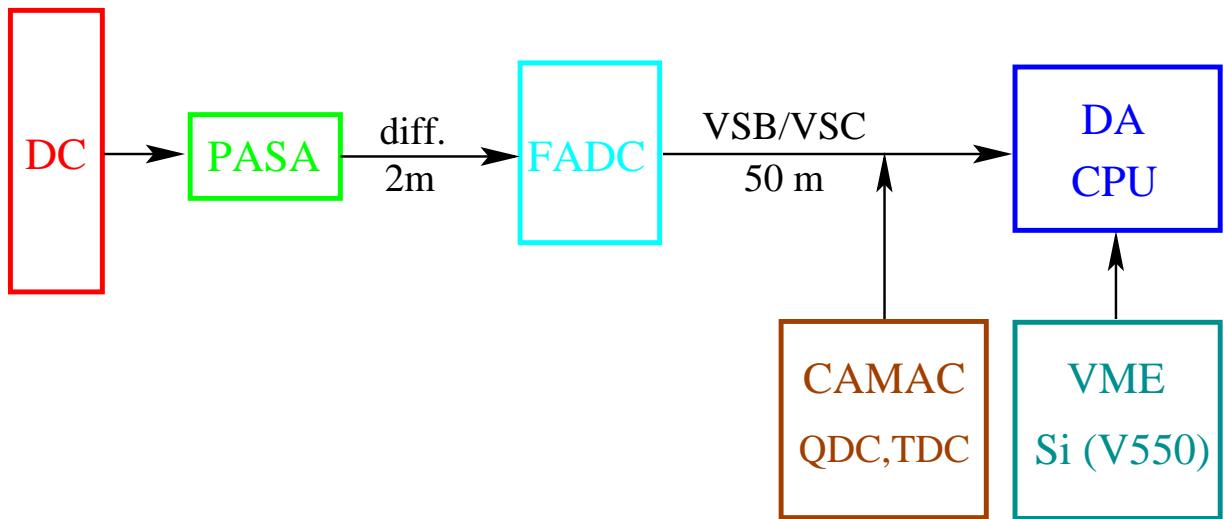
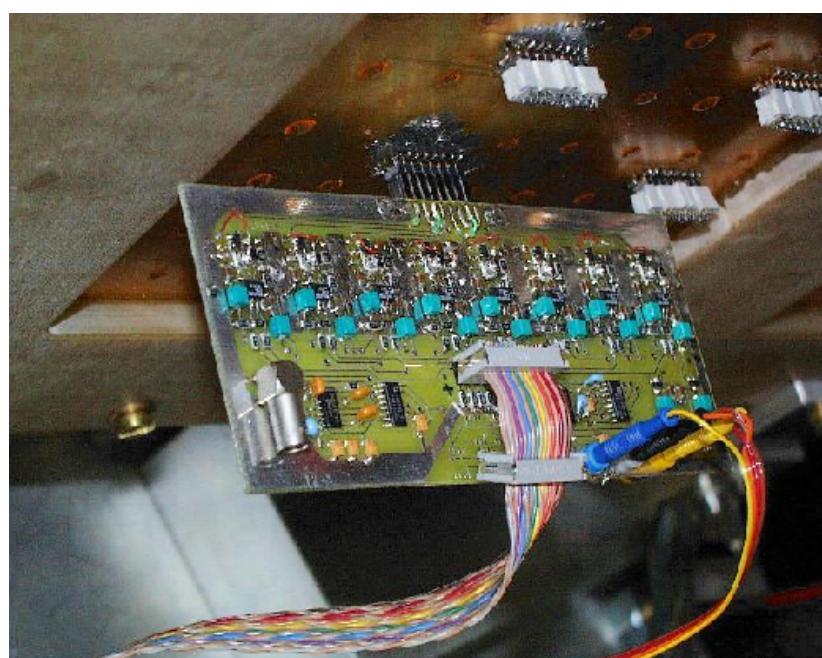


# TRD tests – Electronics and DA

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- **DC** – Drift chamber
  - **PASA** – Preamplifier/Shaper: 2 mV/fC, 1800 e<sup>-</sup> rms, 100 ns FWHM
  - **FADC** – Flash-ADC (Struck), 100 MHz, 8 bit non-lin., 0.6 mV
  - **DA-CPU** – VME (E7, RIO2), LynxOS, MBS DA (GSI standard)
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*current sensitive PA*